



Ph.D. Viva Notification

Mr. Marulasiddappa H. B, Ph.D. Scholar in Electrical Engineering,
will defend his Ph.D. thesis titled

**“Design and Development of an Intelligent Controller Based DTC for PMSM
Drives in an Electric Vehicle”**

in an open viva to be held on

Tuesday, 18th June 2024, 11:00 a.m.

Venue: FET, JAIN (Deemed-to-be University) Kanakapura Campus, Bangalore – 562112

The External Examiner:

Dr. Dinesh M N

Professor - Department of Electrical & Electronics Engineering,
R V College of Engineering, Bangalore - 560059

Chairman & Guide: Dr. V Pushparajesh

Professor, Department of Electrical & Electronics Engineering, FET, JAIN (Deemed-to-be University)

Date: 8th June 2024

Signed by

Dr. Bindhu O S
Deputy Director
Centre for Researcher Training & Administration
JAIN (Deemed-to-be University)